

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	168	345/558.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/06 14:12
L3	94	2 and ((read and write) near3 (queue fifo buffer))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/06 14:12
S2	226	345/519.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 11:10
S3	55	S2 and (graphics near (accelerator controller engine card)) and (memory near controller)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 13:23
S4	17	S3 and packet	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 11:25
S5	9	(Sarath near Kotamreddy).in. or (Tuong near Trieu).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 11:27
S6	54	nvidia and packet	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 11:31
S7	26	S6 and ((integrated near circuit) or (chipset (chip adj set)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 11:31
S8	0	((load and unload) near2 pointer) and (graphics near (accelerator controller engine card)) and (memory near controller)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 13:23
S9	151	((read and write) near2 pointer) and (graphics near (accelerator controller engine card)) and (memory near controller)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 13:25
S10	114	S9 and queue	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 13:30
S11	168	345/558.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 13:24
S12	8	S2 and S9	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 13:25
S13	13	S11 and ((first and second) adj2 clock)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 13:39
S14	14	S9 and ((first and second) adj2 clock)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 13:40

S15	383	(clock near cross\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 14:25
S16	75	S15 and latency.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 14:27
S19	20	S16 and ((read and write) near2 pointer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 15:47
S20	8	(clock near3 cross\$3) same ((read and write) near2 pointer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 15:51
S21	1	((clock near3 cross\$3) and ((read and write) near2 pointer)) same command	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 16:25
S22	1	((clock near3 cross\$3) and (((read and write) (load and unload)) near2 pointer)) same command	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/04 15:52
S28	256	345/520.ccls.	USPAT	OR	ON	2005/05/04 16:43
S29	64	S28 and queue	USPAT	OR	ON	2005/05/04 16:47
S30	1133	((read\$3 and writ\$4) near2 (pointer and buffer))	USPAT	OR	ON	2005/05/04 16:48
S31	101	S30 and (graphics near (card accelerator hardware engine controller))	USPAT	OR	ON	2005/05/04 16:49
S32	26	S31 and ((read\$3 and writ\$4) near2 clock)	USPAT	OR	ON	2005/05/04 16:49
S35	10270	((read and write) near2 (buffer fifo))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 07:59
S36	506	S35 and ((graphics near (card hardware accelerator acceleration engine controller)) and (memory near2 controller))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 07:59
S37	46	S36 and (((first and second) (source and destination) (write and read)) near clock)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 08:41
S38	11545	((read and write) near2 (buffer fifo queue))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 07:59
S39	582	S38 and ((graphics near (card hardware accelerator acceleration engine controller)) and (memory near2 controller))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 08:41
S40	48	S39 and (((first and second) (source and destination) (write and read)) near clock)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 08:00
S45	4182	((graphics near (card hardware accelerator acceleration engine controller)) and (memory near2 controller))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 15:54
S46	229	S45 and (((first and second) (source and destination) (write and read)) near clock)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 08:41

S47	230	S45 and (((first and second) (source and (target destination)) (write and read)) near clock)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 08:42
S48	14	S47 and ((read\$3 and writ\$4) near pointer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 08:50
S50	43	(jeffrey near wilcox).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 08:58
S52	100	345/535.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 09:08
S53	400	(compar\$6 with ((read and write) near pointer))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 09:53
S57	10	S53 and (clock near4 cross\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 09:53
S58	369	(compar\$6 with ((read with write) near pointer))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 13:14
S59	9	S58 and (clock near4 cross\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 11:28
S60	22	S58 and (clock near2 gat\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 09:59
S61	6	S45 and S53	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 10:05
S63	582	S45 and ((read and write) near2 (buffer fifo queue))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 10:06
S64	254	S45 and (((read and write) near2 (buffer fifo queue)) with control\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 10:06
S65	118	S45 and ((read and write) near2 ((buffer fifo queue) and pointer))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 10:07
S72	5	S39 and S53	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 13:11
S73	5236	((graphics near (card hardware accelerator acceleration engine controller)) and (memory near2 control\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 13:14

S74	7	S73 and (compar\$6 with ((read with write) near pointer))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 13:14
S75	7	S73 and (compar\$6 with ((read and write) near pointer))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 13:17
S76	133	S73 and ((read and write) near pointer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 13:18
S77	84	S73 and ((read and write) near (pointer and (fifo queue buffer)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 13:32
S78	201	S73 and ((read and write) near (fifo queue buffer)) and (arbiter (arbitrat\$4 adj2 (circuit unit)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 13:35
S79	9	S78 and (compar\$6 with pointer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 13:35
S81	54	(Creedon near Tadhg).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 15:44
S82	76	((read and write) near2 pointer) and (clock near5 cross\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 15:44
S83	11	((read and write) near2 pointer) with compar\$6) and (clock near5 cross\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 15:45
S84	582	((graphics near (card hardware accelerator acceleration engine controller)) and (memory near2 controller)) and ((read and write) near2 (queue fifo buffer))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 15:55
S85	493	S84 and ((integrated near circuit) chip chipset)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 15:55
S86	109	S85 and ((read\$3 and writ\$4) near2 pointer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 16:13
S87	12	S86 and (((first and second) (write and read)) near clock)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 16:25
S88	10	S86 and (compar\$6 with pointer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 16:27
S89	30	"5,471,583"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/05 16:27